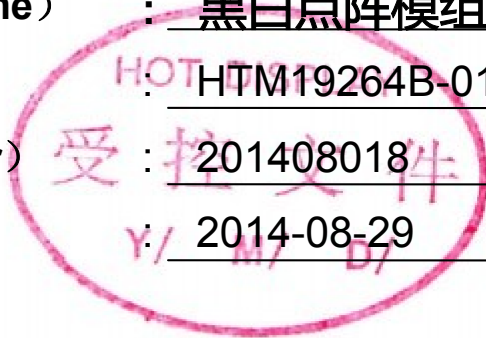




HTM19264B-01Y-N5P

产品名称 (Product name) : 黑白点阵模组
 型号 (Model) : HTM19264B-01Y-N5P
 编号 (Part number) : 201408018
 日期 (Date) : 2014-08-29



深圳市鑫洪泰电子科技有限公司
Shenzhen Hot Display Technology Co.,Ltd

编制 Prepared by	审核 Checked by	核准 Approved by

编码: QR-R-011 A/0

序号:

Rev.	Descriptions	Date
01	Prelimiay Release	2014-08-29

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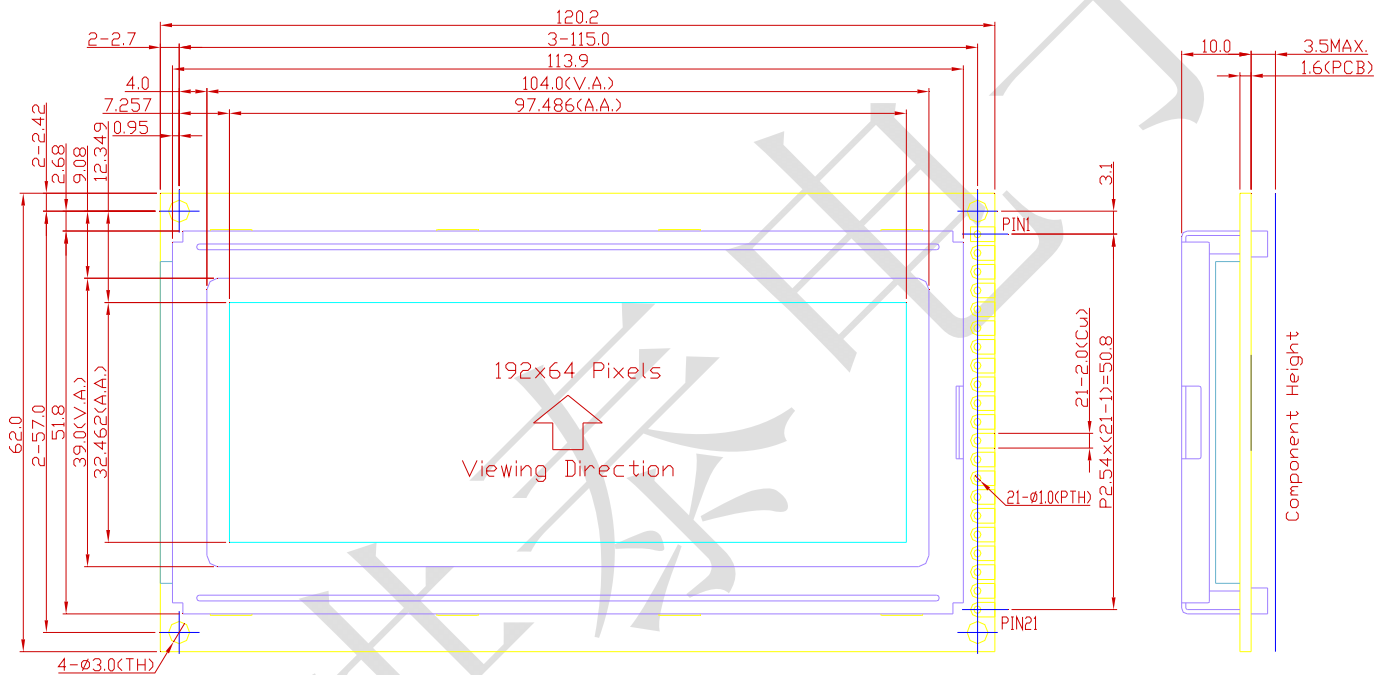
1. Basic Specifications

1.1 Display Specifications

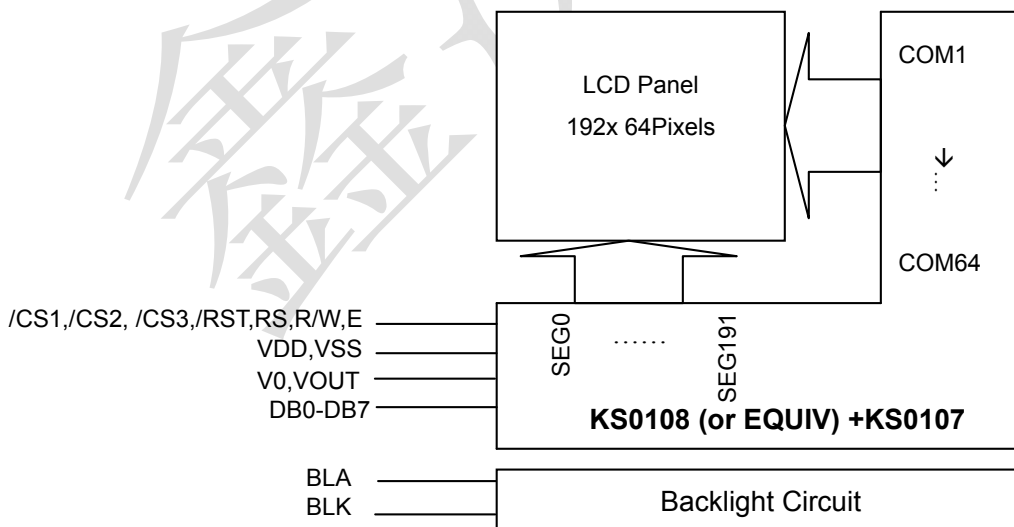
- 1>LCD Display Mode : STN-YG, Positive, ,Transflective
- 2>Viewing Angle : 6H
- 3>Driving Method : 1/64 Duty, 1/9 Bias
- 4 >Backlight : Yellow Green

1.2 Mechanical Specifications

- 1>Outline Dimension : 120.2X62.0 X 13.5 (MAX) mm (See attached Outline Drawing for Data)

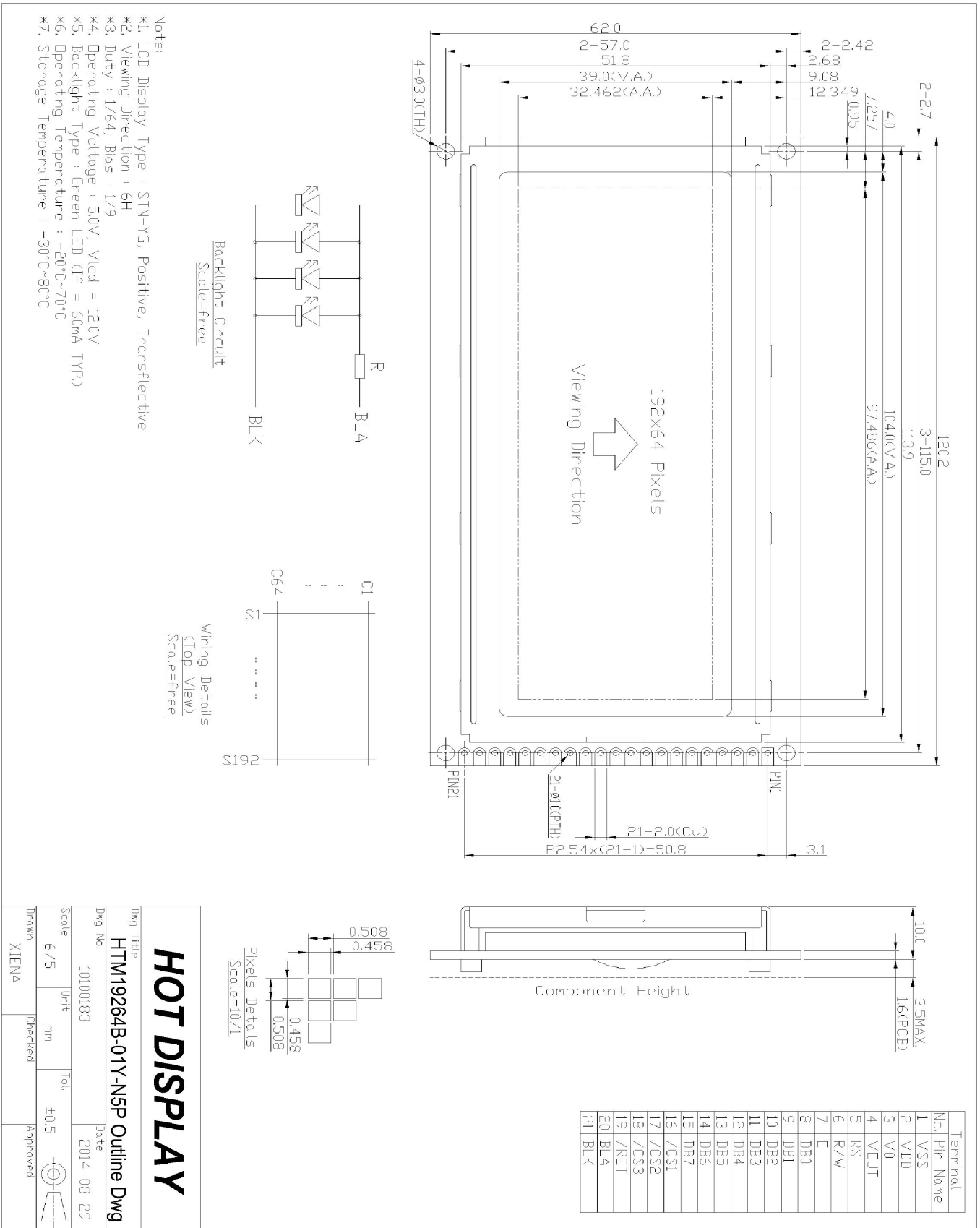


1.3 Circuit Diagram

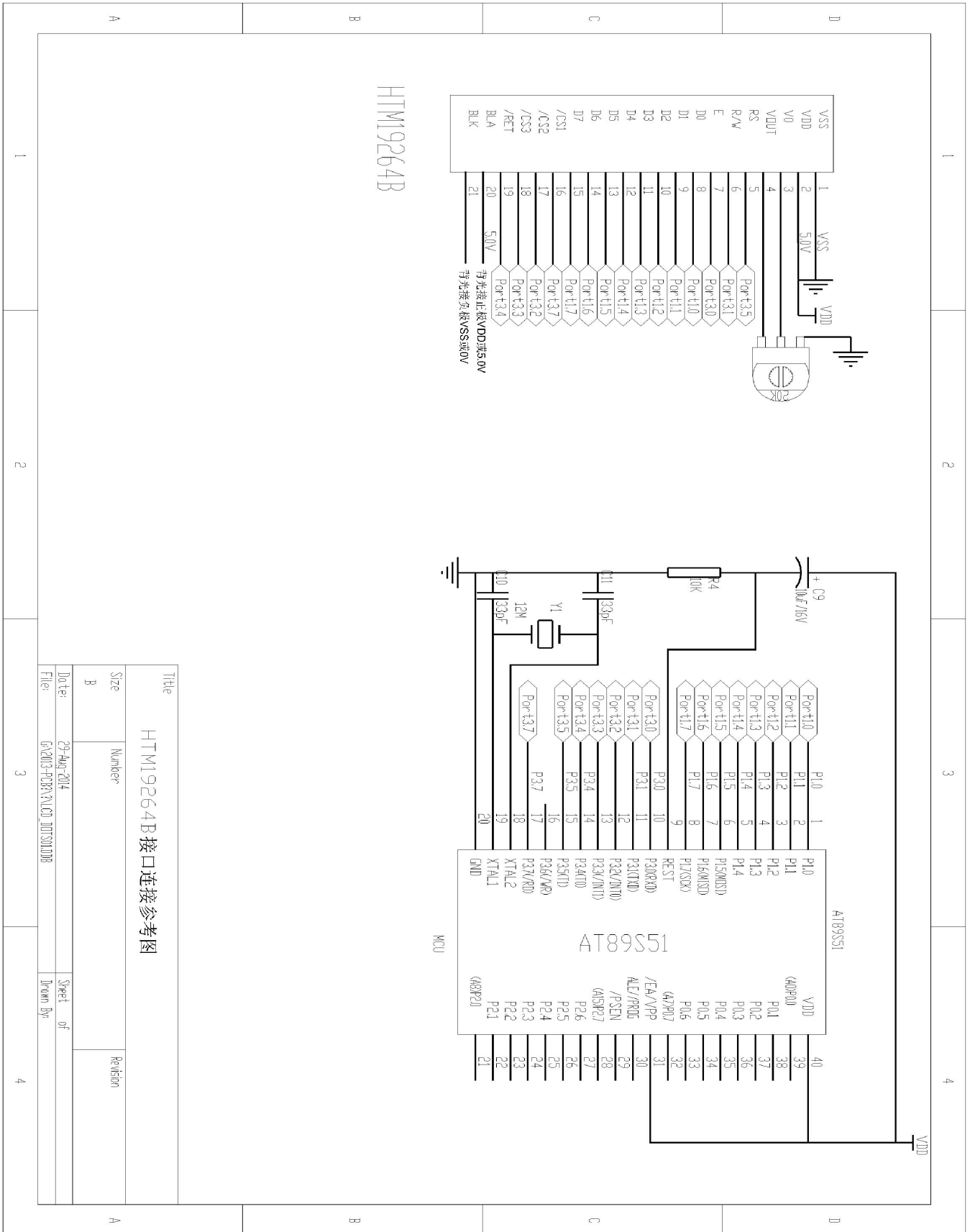


1.4 Terminal Function

Pin No.	Pin Name	Function
1	VSS	Power Supply Negative(0V)
2	VDD	Power Supply Positive(5.0V)
3	V0	LCM Adjust Contrast
4	VOOUT	LCD Driver, About(-10)V
5	RS	Data or Instruction. RS=H->DB<0:7> : Display RAM Data RS=L->DB<0:7> : Instruction Data
6	R/W	H:Read ; L: Write. R/W=H-> Data appears at DB<0:7> and can be read by the CPU while E=H, CS1=L, CS2=L and CS3=H. R/W=L->Display data DB<0:7> can be written at falling of E when CS1B=L, CS2=L and CS3=H
7	E	Enable signal. write mode (R/W=L) -> data of DB<0:7> is latched at the falling edge of E. read mode (R/W=H) -> DB<0:7> appears the reading data while E is at high level.
8~15	DB0~DB7	Data Buss
16	/CS1	Chip selection input 1(Left)
17	/CS2	Chip selection input 2(Mid)
18	/CS3	Chip selection input 3 (Right)
19	/RET	Reset Signal input
20	BLA	Backlight Positive(5.0V)
21	BLK	Backlight Positive(Negative)

1.5 Product Outline


1.6 Schematic Diagram



HTM19264B 接口连接参考图

Title		Revision	
HTM19264B 接口连接参考图			
Size	Number		
B			
Date:	29-Aug-2014	Sheet	of
File:	G:\2013-FCR\VALCO_INT\SCHIBB	Drawn by:	

2. Absolute Maximum Ratings

Items	Symbol	MIN.	MAX.	Unit	Condition
Supply Voltage	V _{DD}	-	+5.5	V	V _{SS} = 0V
Supply Voltage	V _{EE}	V _{DD} -16.0	V _{DD} +0.3	V	V _{SS} = 0V
Driver Supply Voltage	V _{LCD}	V _{EE} -0.3	V _{DD} +0.3	V	-
Operating Temperature	T _{OP}	-20	+70	°C	No Condensation
Storage Temperature	T _{st}	-30	+80	°C	No Condensation

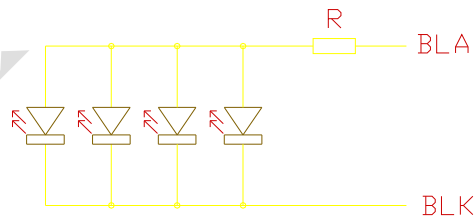
3. Electrical Characteristics

3.1 DC Characteristics

Items	Symbol	MIN.	TYP.	MAX.	Unit	Condition
Operating Voltage	V _{DD}	4.5	5.0	5.5	V	V _{DD}
Input High Voltage	V _{IH}	2.0	-	V _{DD}	V	*1
Input Low Voltage	V _{IL}	0	-	0.8	V	
Output High Voltage	V _{OH}	2.4	-	-	V	*2
Output Low Voltage	V _{OL}	-	-	0.4	V	
Operating Current	I _{DD1}	-	2.0	3.0	mA	*3
OSC Frequency	F _{OSC}	315	450	585	KHz	R _f =47K±2%

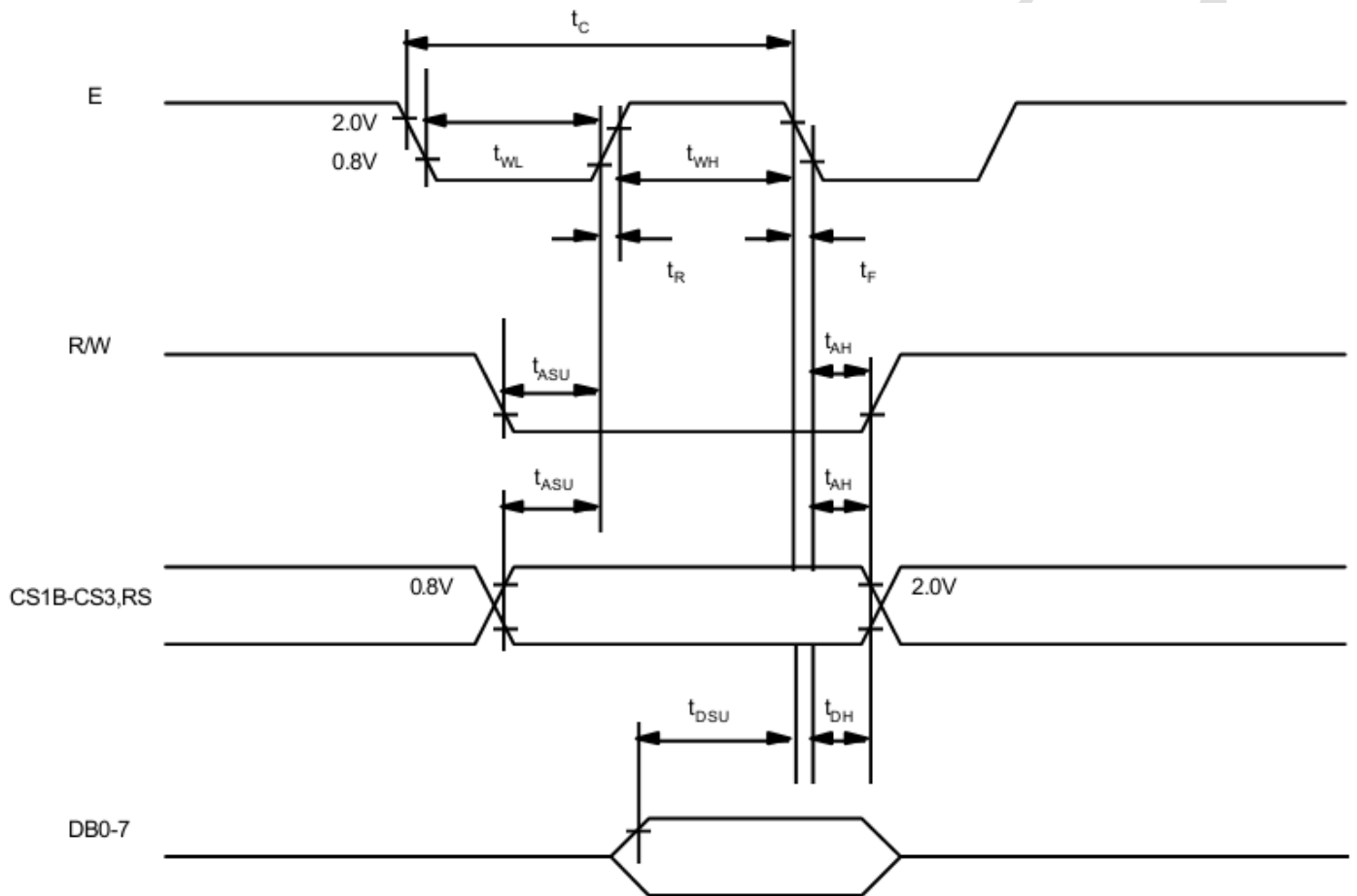
3.2 LED Backlight Circuit

Items	Symbol	MIN.	TYP.	MAX.	Unit	Condition
Forword Voltage	V _f BLA	-	5.0	-	V	Without Resister
Forword Current	I _f BLA	-	45	60	mA	-

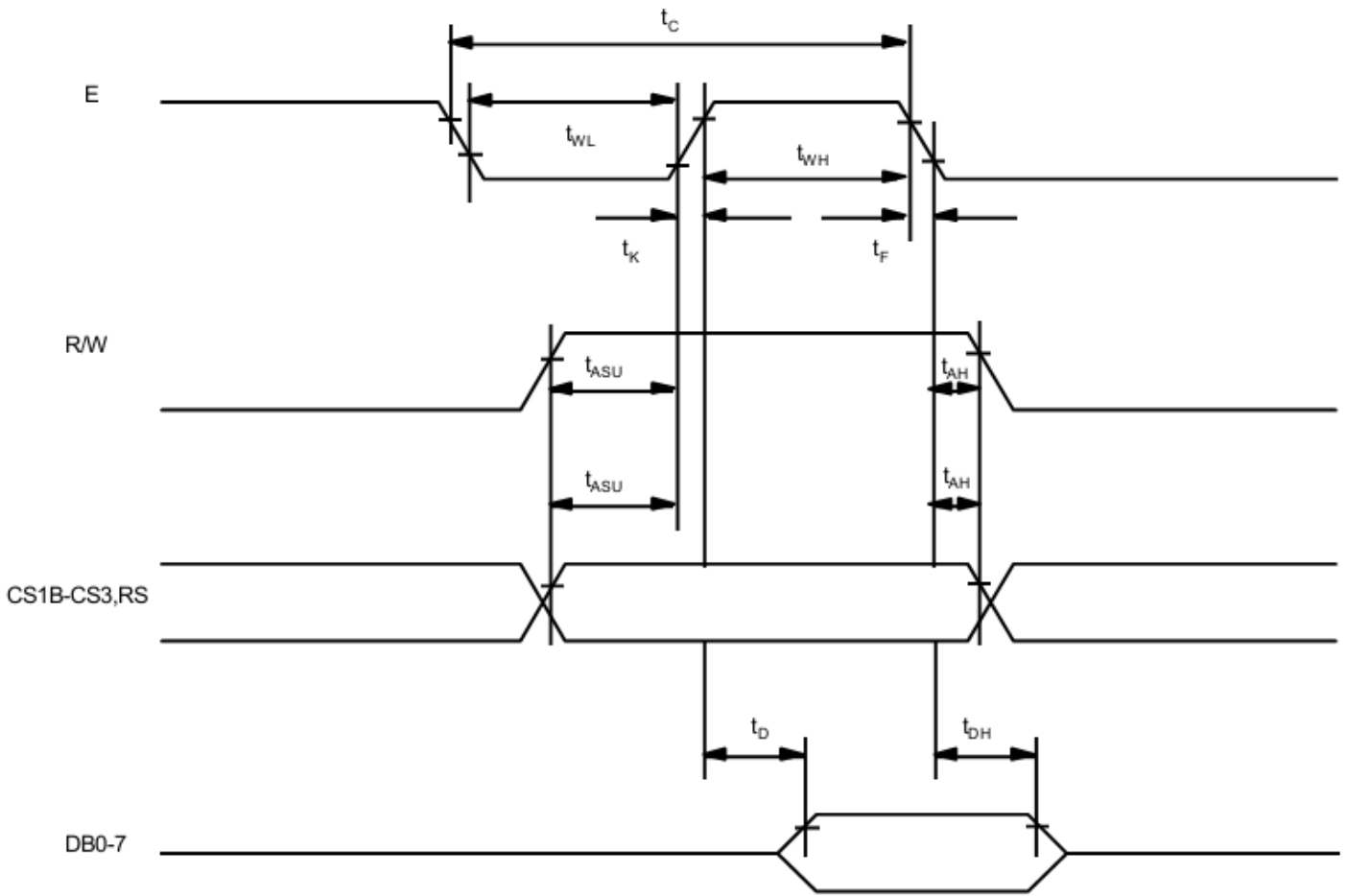


3.3 AC Characteristics

Chatacteristic	Symbol	Min	Typ	Max	Unit
E Cycle	t_C	1000	-	-	ns
E High Level Width	t_{WH}	450	-	-	ns
E Low Level Width	t_{WL}	450	-	-	ns
E Rise Time	t_R	-	-	25	ns
E Fall Time	t_F	-	-	25	ns
Address Set-Up Time	t_{ASU}	140	-	-	ns
Address Hold Time	t_{AH}	10	-	-	ns
Data Set-Up Time	t_{DSU}	200	-	-	ns
Data Delay Time	t_D	-	-	320	ns
Data Hold Time (Write)	t_{DHW}	10	-	-	ns
Data Hold Time (Read)	t_{DHR}	20	-	-	ns



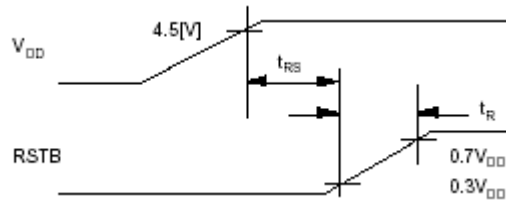
MPU write timing



MPU read timing

3.4 Rest Timing

Item	Symbol	Min	Typ	Max	Unit
Reset Time	t_{RS}	1.0	-	-	us
Rise Time	t_R	-	-	200	ns



4. Function specifications

4.1 The Parallel Interface

	Area Selection			Shared	6800 Mode			Function(6800)
	/CS3	/CS2	/CS1		RS	R/W	E	
Left (0)	1	1	0	H	H	H	Reads the display data	
Mddle(1)	1	0	1	H	L	H→L	Writes the display data	
Right(2)	0	1	1	L	H	H	Staus read	
				L	L	H→L	Write Command data	

4.2 Display Memory Map

PAGE	COL						
	SEG0	SEG1	SEG2	-----	SEG189	SEG190	SEG191
PAGE0	D7 D0	D7 D0		-----		D7 D0	D7 D0
PAGE1				Pixels:192X64			
PAGE6							
PAGE7	D7 D0	D7 D0		-----		D7 D0	D7 D0

4.3 Display Commands

Instruction	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Function	
Display ON/OFF	L	L	L	L	H	H	H	H	H	L/H	Controls the display on or off. Internal status and display RAM data is not affected. L:OFF, H:ON	
Set Address	L	L	L	H	Y address (0~63)						Sets the Y address in the Y address counter.	
Set Page (X address)	L	L	H	L	H	H	H	Page (0~7)			Sets the X address at the X address register.	
Display Start Line	L	L	H	H	Display start line (0~63)						Indicates the display data RAM displayed at the top of the screen.	
Status Read	L	H	B U S Y	L	O N / O F F	R E S E T	L	L	L	L	Read status. BUSY L: Ready H: In operation ON/OFF L: Display ON H: Display OFF RESET L: Normal H: Reset	
Write Display Data	H	L	Write Data									Writes data (DB0:7) into display data RAM. After writing intruction, Y address is increased by 1 automatically.
Read Display Data	H	H	Read Data									Reads data (DB0:7) from display data RAM to the data bus.

Reference code

```

void Cselect()
{
if(chip_select==0)
    { CS1=0;CS2=1;CS3=1;}
if(chip_select==1)
    { CS1=1;CS2=0;CS3=1;}
if(chip_select==2)
    { CS1=1;CS2=1;CS3=0;
    }
}
void WrateC(uchar cmd)
{
Cselect();
R_S=0;
RW=0;
P1=cmd;
E=1;
E=0;
CS1=1;CS2=1;CS3=1;
}

```

```

void WriteData(uchar dat)
{
Cselect();
R_S=1;
RW=0;
P1=dat;
E=1;
E=0;
CS1=1;CS2=1;CS3=1;
}

void Setadd(uchar
column,uchar page)
{
if(column<64)
chip_select=0;
else
{
if(column>127)
{ chip_select=2;

```

```

column-=127;}
else
{ chip_select=1; column-=63; }
}

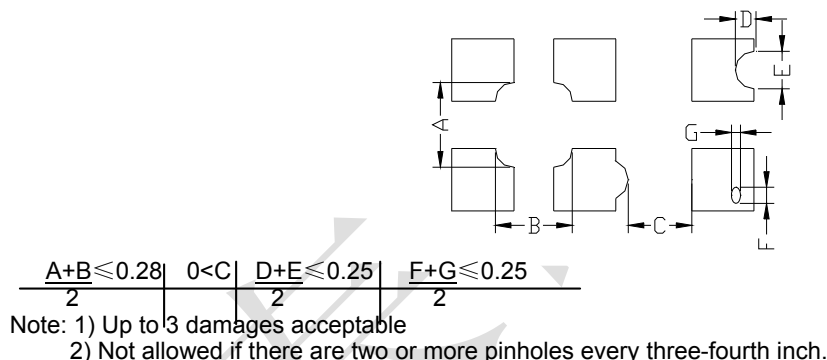
WrateC(page+0xB8);
WrateC(column+0x40);
}

```

4.4 Basic Operating Sequence Initialization Sequence

	Code Function										Note	
	A0	D7	D6	D5	D4	D3	D2	D1	D0	hex		
Turn on Power Supply VDD & VSS While maintaining /RES at LOW	-	-	-	-	-	-	-	-	-	-	-	
Wait until power supply is stabilized	-	-	-	-	-	-	-	-	-	-	-	
Release the /RES Reset Signal (/RES = High)	-	-	-	-	-	-	-	-	-	-	-	
START LINE	0	1	1	0	0	0	0	0	0	0	C0H	Sets the X address at the X address register.
DISPLAY ON	0	0	0	1	1	1	1	1	1	1	3FH	
SET PAGE	0	1	0	1	1	8	0	0	0	0	B8H	Sets the X address at the X address register.
SET ADDRESS	0	0	1	0	0	0	0	0	0	0	40H	Sets the Y address in the Y address counter.
WRITE DATA	1	X	X	X	X	X	X	X	X	X	XXH	DISPLAY DATA

5. Inspection Standards

Item	Criterion for defects	Defect type
1) Display on inspection	(1) Non display (2) Vertical line is deficient (3) Horizontal line is deficient (4) Cross line is deficient	Major
2) Black / White spot	Size Φ (mm) $\Phi \leq 0.3$ Acceptable number $0.3 < \Phi \leq 0.45$ Ignore (note) $0.45 < \Phi \leq 0.6$ 3 $0.6 < \Phi$ 1 0	Minor
3) Black / White line	Length (mm) Width (mm) Acceptable number $L \leq 10$ $W \leq 0.03$ Ignore $5.0 \leq L \leq 10$ $0.03 < W \leq 0.04$ 3 $5.0 \leq L \leq 10$ $0.04 < W \leq 0.05$ 2 $1.0 \leq L \leq 10$ $0.05 < W \leq 0.06$ 2 $1.0 \leq L \leq 10$ $0.06 < W \leq 0.08$ 1 $L \leq 10$ $0.08 < W$ follows 2) point defect Defects separate with each other at an interval of more than 20mm	Minor
4) Display pattern	 <p>Note: 1) Up to 3 damages acceptable 2) Not allowed if there are two or more pinholes every three-fourth inch.</p>	Minor
5) Spot-like contrast irregularity	Size Φ (mm) Acceptable Number $\Phi \leq 0.7$ Ignore (note) $0.7 < \Phi \leq 1.0$ 3 $1.0 < \Phi \leq 1.5$ 1 $1.5 < \Phi$ 0 Note: 1) Conformed to limit samples. 2) Intervals of defects are more than 30mm.	Minor
6) Bubbles in polarizer	Size Φ (mm) Acceptable Number $\Phi \leq 0.4$ Ignore (note) $0.4 < \Phi \leq 0.65$ 2 $0.65 < \Phi \leq 1.2$ 1 $1.2 < \Phi$ 0	Minor
7) Scratches and dent on the polarizer	Scratches and dent on the polarizer shall be in the accordance with "2) Black/white spot", and "3) Black/White line".	Minor
8) Stains on the surface of LCD panel	Stains which cannot be removed even when wiped lightly with a soft cloth or similar cleaning.	Minor
9) Rainbow color	No rainbow color is allowed in the optimum contrast on state within the active area.	Minor
10) Viewing area encroachment	Polarizer edge or line is visible in the opening viewing area due to polarizer shortness or sealing line.	Minor
11) Bezel appearance	Rust and deep damages that are visible in the bezel are rejected.	Minor
12) Defect of land surface contact	Evident crevices that are visible are rejected.	Minor
13) Parts mounting	(1) Failure to mount parts (2) Parts not in the specifications are mounted (3) For example: Polarity is reversed, HSC or TCP falls off.	Minor
14) Part alignment	(1) LSI, IC lead width is more than 50% beyond pad outline. (2) More than 50% of LSI, IC leads is off the pad outline.	Minor
15) Conductive foreign matter (solder ball, solder hips)	(1) $0.45 < \Phi$, $N \geq 1$ (2) $0.3 < \Phi \leq 0.45$, $N \geq 1$, Φ : Average diameter of solder ball (unit: mm) (3) $0.5 < L$, $N \geq 1$, L: Average length of solder chip (unit: mm)	Minor
16) Bezel flaw	Bezel claw missing or not bent	Minor
17) Indication on name plate (sampling indication label)	(1) Failure to stamp or label error, or not legible.(all acceptable if legible) (2) The separation is more than 1/3 for indication discoloration, in which the characters can be checked.	Minor

6. Handling Precautions

6.1 Mounting method

A panel of LCD module made by our company consists of two thin glass plates with polarizers that easily get damaged. And since the module is so constructed as to be fixed by utilizing fitting holes in the printed circuit board (PCB), extreme care should be used when handling the LCD modules.

6.2 Cautions of LCD handling and cleaning

When cleaning the display surface, use soft cloth with solvent (recommended below) and wipe lightly.

- Isopropyl alcohol
- Ethyl alcohol
- Trichlorotrifluoroethane

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Ketene
- Aromatics

6.3 Caution against static charge

The LCD module uses C-MOS LSI drivers. So we recommend you:

Connect any unused input terminal to V_{dd} or V_{ss} . Do not input any signals before power is turned on, and ground your body, work/assembly areas, assembly equipment to protect against static electricity.

6.4 Packaging

- Module employs LCD elements, and must be treated as such. Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity.

6.5 Caution for operation

-It is an indispensable condition to drive LCD module within the limits of the specified voltage since the higher voltage over the limits may cause the shorter life of LCD module.

-An electrochemical reaction due to DC (direct current) causes LCD undesirable deterioration so that the uses of DC (direct current) drive should be avoided.

-Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD module may show dark color in them. However those phenomena do not mean malfunction or out of order of LCD module, which will come back in the specified operating temperature.

6.6 Storage

In the case of storing for a long period of time, the following ways are recommended:

- Storage in polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with not desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light is. Keeping the storage temperature range.
- Storing with no touch on polarizer surface by any thing else.


6.7 Safety

-It is recommendable to crash damaged or unnecessary LCD into pieces and to wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.

-When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well at once with soap and water.

7. Packaging Specifications



	<h2>Packaging Specifications</h2> <h3>HTM19264B</h3>	Approved	Checked	Designed

7.1 Packaging Material

No	Item	Dimensions (mm)	IPCS Weight (KG)	Quantity	Total Weight
1	LCM	120.0*62.0*14.6	0.095	128	18.2
2	PE Bag	150*100	0.001	128	0.2
3	Foam Rubber Cushion	310*170	0.0175	8	0.21
4	Partition Al	310*200*100	0.30	4	1.2
5	Product Box	330*180*120 (neutral packing)	0.45	4	1.8
6	Carton	390*370*350 (neutral packing)	0.9	1	0.9
7	Tape			AR	
8	Label Specifications			1	
9	Label Rohs			1	
10	Label ESD			1	

7.2. Total LCD Weight in carton: 22.5 KG ± 10%

7.3. Packaging Specifications and Quantity:

(1) Quantity Of Spacer: A2*4

(2) Total LCM quantity in carton: quantity per box 32* no of boxes 4 = 128

